



SHEET 1 OF 1

LIST OF ART CITED BY APPLICANT
(PTO-1449)ATTY. DOCKET NO.
INTEL-0044APPLN. SERIAL NO.
10/721,184APPLICANT(S)
Stephen TANG et al.FILING DATE
November 26, 2003GROUP
2827**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	*PATENT NO.	*ISSUE DATE	*INVENTOR NAME	CLASS	SUBCLASS	FILING DATE

U.S. PATENT APPLICATION PUBLICATIONS

	*PATENT APPLN. PUB. NO.	*PUB. DATE	*APPLICANT	CLASS	SUBCLASS	

U.S. PATENT APPLICATIONS

	*APPLN. NO.	*FILING DATE	*INVENTOR	CLASS	SUBCLASS	

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Publisher, Place of Publication, Etc.)

WJ	WANN et al., A Capacitorless DRAM Cell on SOI Substrate, 1993 IEEE, pp. 26.4.1-26.4.4

EXAMINER

Huan Hoang

DATE CONSIDERED

11/14/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.
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